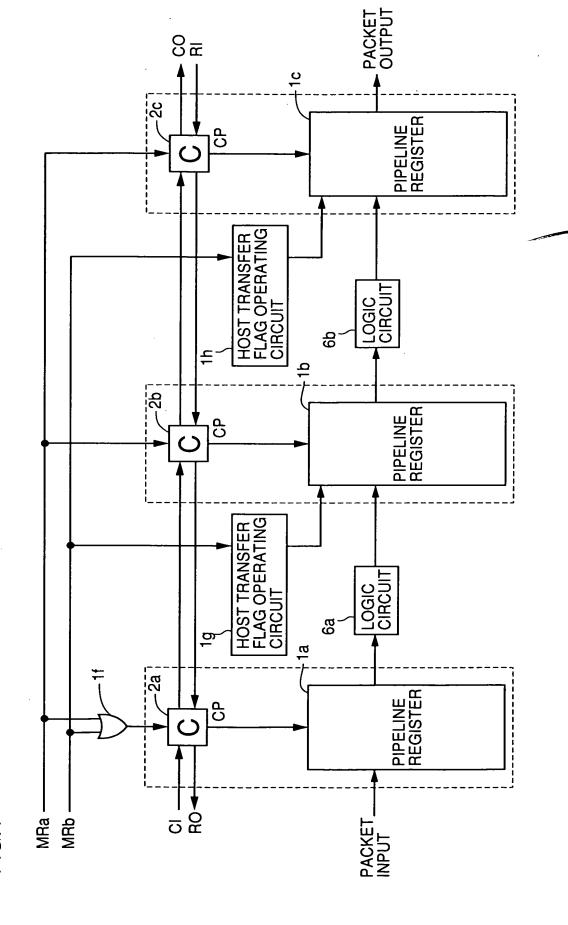
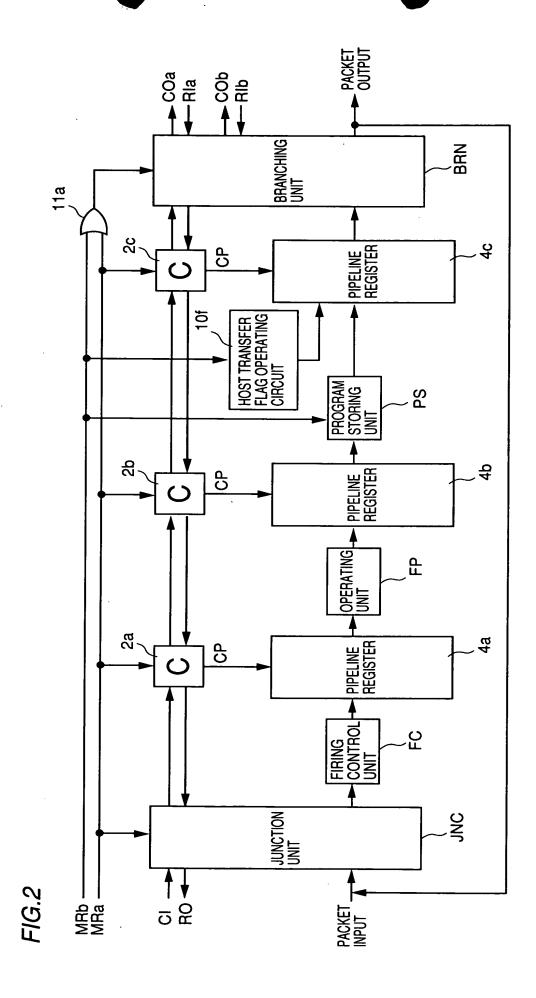
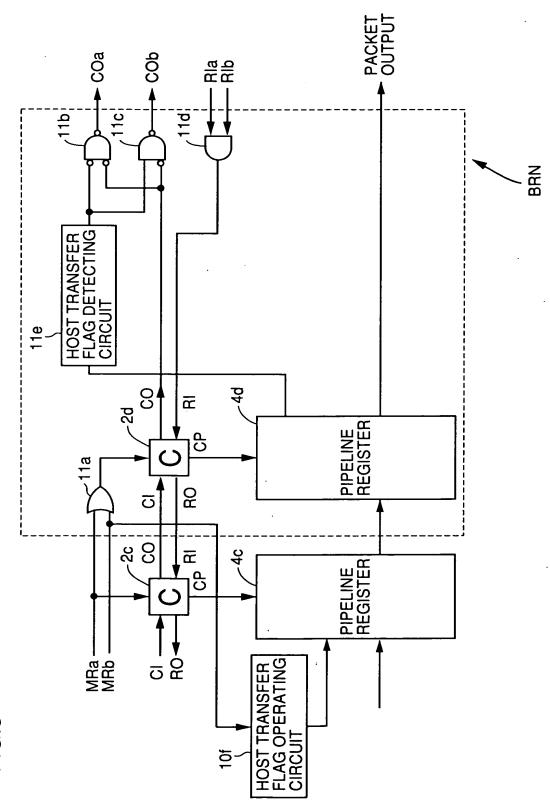
4 · 1



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 $a=i\in\mathcal{K}-j$ 



F1G.3

HOST TRANSFER FLAG OPERATING-CIRCUIT 2g LOGIC 99 <del>-</del> 16 -2b PIPELINE REGISTER S LOGIC CIRCUIT <u>-</u>1 -2a PIPELINE REGISTER S MRa -MRb F1G.4

8=

720

<del>ا</del>

S

PIPELINE REGISTER

FIG.5 PRIOR ART

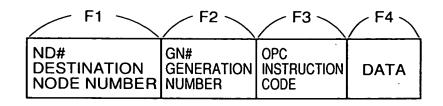
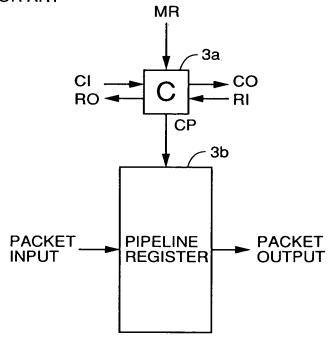


FIG.6 PRIOR ART



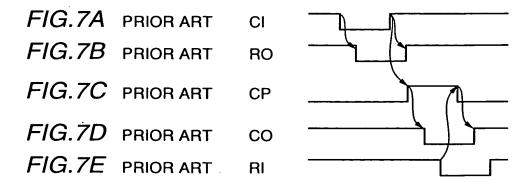


FIG.8 PRIOR ART

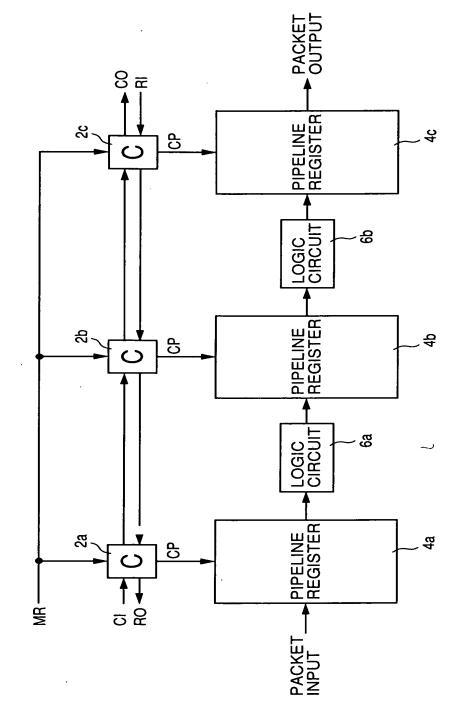
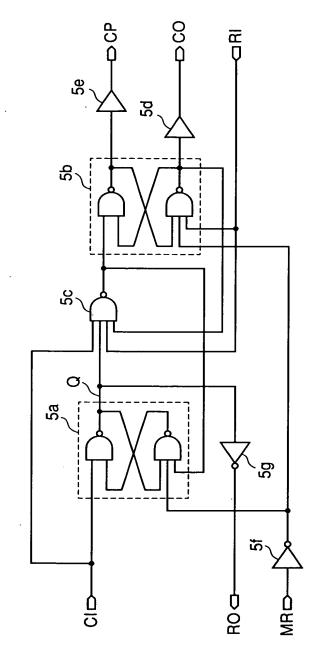


FIG.9 PRIOR ART



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BRANCHING BRN გ. PIPELINE REGISTER S PROGRAM STORING UNIT PS 8 PIPELINE REGISTER <del>4</del>6 S OPERATING UNIT 괍 **2**a PIPELINE -REGISTER **4**a S O FIRING CONTROL UNIT N C FIG. 10 PRIOR ART JUNCTION S 2 & | ↓ ÄΑ̈́ PACKET. INPUT

PACKET

0 E

7. A. Berry

FIG.11 PRIOR ART

